A Tree Clock Data Structure for Causal Orderings in Concurrent Executions

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Andreas Pavlogiannis

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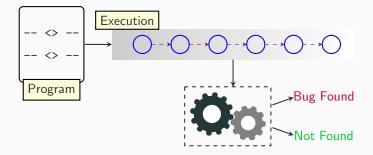


Concurrency: Software and Challenges

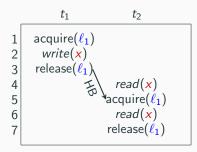


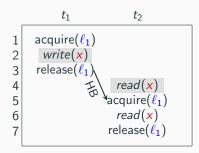
- Ubiquitous computing paradigm.
- Analysis of concurrent programs is a major challenge.
- We need more **efficient** algorithms and data structures.

Dynamic Analyses for Detecting Concurrency Bugs

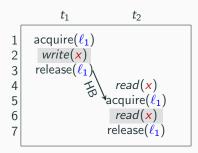


- Widely adopted (e.g., ThreadSanitizer, Helgrind).
- Requires establishing a causal ordering between the events.
- Causality is typically represented as a partial order.





 \rightarrow Events e_2 and e_4 are concurrent.



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Happens-Before defines data races in various memory models.

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- Versatile data structure.
 - Other partial orders can also be computed efficiently.
 - Schedulable-Happens-Before
 - Mazurkiewicz
- Significant speedups compared to vector clocks.

• The knowledge set of a thread t can be succinctly captured by a function:

 V_t : Threads $\rightarrow \mathbb{N}$

- $V_t(t')$ gives the last event of t' that t knows about.
- t knows about all preceding events as well.

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 $V_{t_2} = [\overset{t_1}{\textbf{27}}, \overset{t_2}{\textbf{3}}, \overset{t_3}{\textbf{9}}, \overset{t_4}{\textbf{45}}, \overset{t_5}{\textbf{17}}, \overset{t_6}{\textbf{26}}]$

- t_2 knows of the first 27 events of t_1 .
- t_2 has performed **3** events.

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Operations

$$\begin{array}{lll} \mathsf{V}_1 \sqsubseteq \mathsf{V}_2 & \text{iff} & \forall t \colon \mathsf{V}_1(t) \leq \mathsf{V}_2(t) & (\text{Comparison}) \\ \mathsf{V}_1 \sqcup \mathsf{V}_2 & = & \lambda t \colon \max(\mathsf{V}_1(t),\mathsf{V}_2(t)) & (\text{Join}) \end{array}$$

Background: Implementing Vector Timestamps

Just use a vector clock

$$VC_t = [27, 3, 9, 45, 17, 26]$$

Vector Clock Join $VC_1 \leftarrow VC_1 \sqcup VC_2$

- For each thread t:
 - If VC₁[*t*] < VC₂[*t*]
 - $VC_1[t] \leftarrow VC_2[t]$

Vector Clock Copy $VC_1 \leftarrow VC_2$

- For each thread t:
 - $VC_1[t] \leftarrow VC_2[t]$

Each operation takes $O(\mathcal{T})$ time, for \mathcal{T} threads

Background: Computing Happens-Before with Vector Clocks

- One vector clock \mathbb{C}_t per thread t
- One vector clock \mathbb{C}_ℓ per lock ℓ

Algorithm: Happens-Before (HB)

1 **procedure** acquire(t, ℓ)

2 $\mathbb{C}_t \leftarrow \mathbb{C}_t \sqcup \mathbb{C}_\ell;$ /* Vector clock join */

3 procedure release(t, ℓ)

4 $\mathbb{C}_\ell = \mathbb{C}_t;$ /* Vector clock copy */

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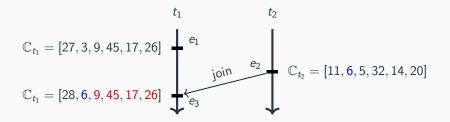
3 **procedure** release(t, ℓ)

4 $\mathbb{C}_{\ell} = \mathbb{C}_t;$ /* Vector clock copy */

- Every vector clock operation costs $O(\mathcal{T})$
 - \mathcal{T} is the number of threads
- When threads are many, the complexity is quadratic $O(N \cdot T)$
 - $\ensuremath{\mathcal{N}}$ is the number of acquire/release events

Overhead of Vector Clocks

- Every vector clock join takes $O(\mathcal{T})$ time.
- Certain steps in the join operation can be vacuous.



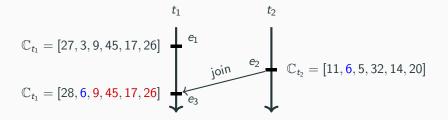
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Can we do sub-linear joins?

 \rightarrow Sub-linear means skip looking at certain entries. How?

 \rightarrow Tree clocks address this challenge.



Our Contribution: Tree Clock Data Structure

- Drop-in replacement of vector clocks.
- Tree clocks maintain information hierarchically.
 - Nodes store local times of a thread + metadata.
 - Tree structure records how information has been obtained transitively.

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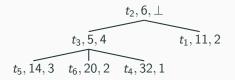
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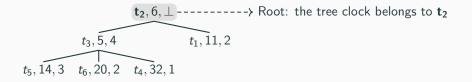
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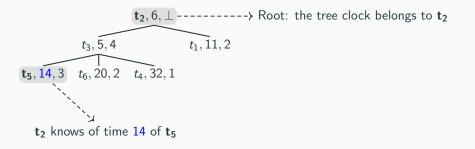
$$\mathbb{C}_{t_4} = \begin{bmatrix} t_1 & t_2 & t_3 & t_4 \\ 1 & 2 & 13 & 9 \end{bmatrix} \longrightarrow t_3, 13, 5 \quad t_2, 2, 1$$

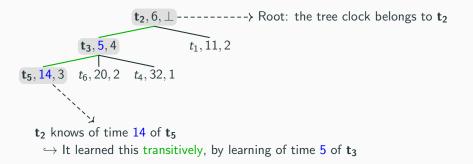
• Only slightly more information is stored compared to vector clocks.

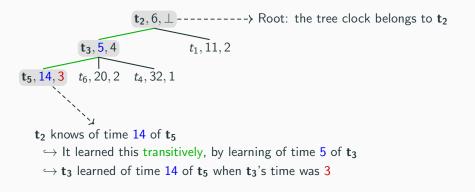
Enough to enable tree clocks to support sub-linear join and (monotone) copy!

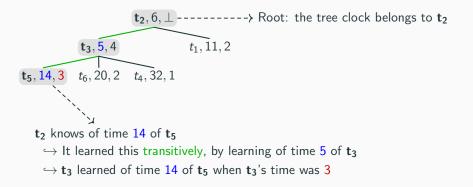








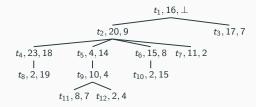


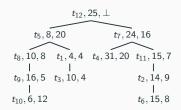


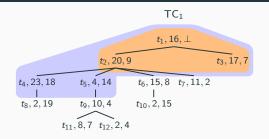
This structure allows for sub-linear time join and (monotone) copy.

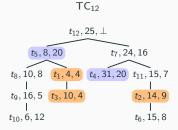
 TC_1





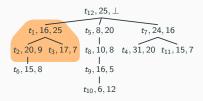


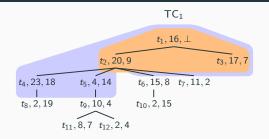


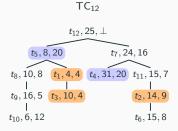




 TC_{12} .Join (TC_1)

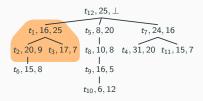




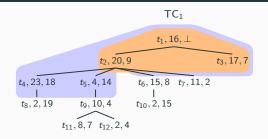


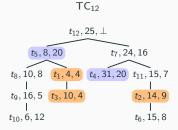
Accessed + Updated Accessed

Performed join without accessing the whole tree!



 TC_{12} .Join (TC_1)





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Performed join without accessing the whole tree!



 $\begin{array}{c} t_{12}, 25, \bot \\ \hline t_{1}, 16, 25 \\ t_{2}, 20, 9 \\ t_{3}, 17, 7 \\ t_{6}, 15, 8 \\ t_{1}, 16, 5 \\ t_{1}, 16, 5 \\ t_{1}, 10, 8 \\ t_{2}, 10, 8 \\ t_{1}, 10, 8 \\ t_{1}, 10, 5 \\ t_{1}, 10, 5 \\ t_{1}, 10, 6, 12 \end{array}$

 TC_{12} .Join (TC_1)

Drop-in Replacement

Algorithm: Happens-Before with Vector Clocks.

- 1 **procedure** acquire(t, ℓ)
- 2 \mathbb{C}_t .VectorClockJoin (\mathbb{C}_ℓ)
- 3 procedure release(t, ℓ)
- 4 \mathbb{C}_{ℓ} .VectorClockCopy (\mathbb{C}_t)

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Treeclock optimality for Happens-Before

No other data structure can offer asymptotically better performance.

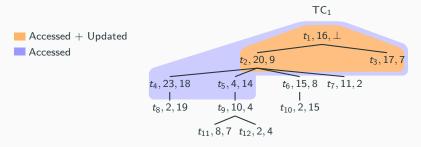
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• Tree clocks perform at most 3 times more work than necessary.

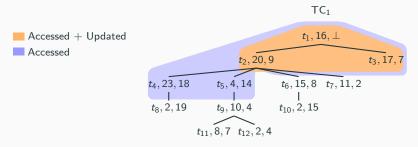
Tree Clock Optimality



Vector time work $VTWork(\sigma)$

VTWork(σ) = the *smallest* number of data-structure accesses for processing σ

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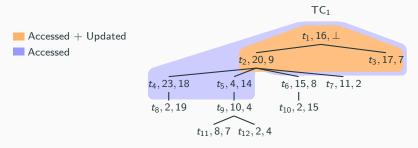
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 $\mathsf{TCWork}(\sigma) = \mathsf{the total number of } tree \operatorname{clock entries accessed for processing } \sigma$

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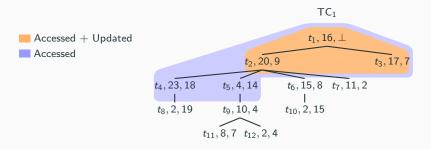
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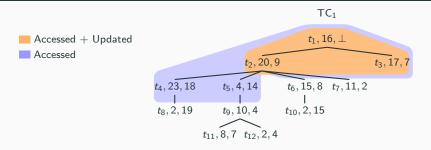
Data Structure Optimality for Happens-Before



 $\mathsf{VCWork}(\sigma) \leq \mathcal{T} \cdot \mathsf{VTWork}(\sigma)$

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Theorem

 $\mathsf{TCWork}(\sigma) \leq 3 \cdot \mathsf{VTWork}(\sigma)$

Tree clocks are (asymptotically) VT-optimal!

Beyond Happens-Before

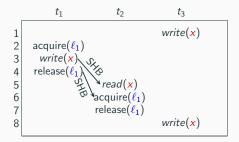
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Schedulable-Happens-Before (SHB)

- Used in sound data race detection¹



¹U. Mathur, D. Kini, M. Viswanathan. What Happens-after the First Race? Enhancing the Predictive Power of Happens-before Based Dynamic Race Detection. OOPSLA'18.

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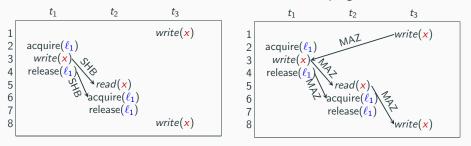
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Schedulable-Happens-Before (SHB)

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Mazurkiewicz (MAZ)

 Used in dynamic partial order reduction in model checking of concurrent programs².



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²C. Flanagan, P. Godefroid. Dynamic Partial-Order Reduction for Model Checking Software.
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- 153 benchmark traces
 - Based on standard Java and OpenMP benchmark suites.
- Implemented HB, SHB and MAZ with both tree clocks and vector clocks.
- Measured the time in the following tasks:
 - Compute the partial order.
 - Perform the race-detection analysis.



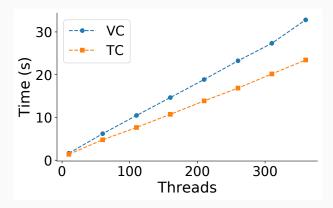
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	Mazurkiewicz	Schedulable-	Happens-Before
		Happens-Before	
Partial Order	2.02×	2.66×	2.97×
Partial Order + Analysis	1.49×	1.80 imes	1.11 imes

Significant speedup by just replacing vector clocks with tree clocks!

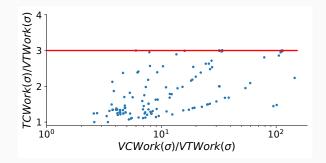
Experimental Results - Scalability

- Controlled experiment: threads randomly communicate over a single lock.
 - Theoretical speedup: $4 \times$
 - Observed speedup: $1.33\times$



Theorem

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Thank you!